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IN THE CLAIMS:RECEIVED
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1. (Currently Amended) A display device comprising:

a ~~plurality of source signal lines~~ line;a ~~plurality of gate signal lines~~ line;a common electrode; anda ~~plurality of pixels arranged in matrix~~, each of the ~~pixels~~ pixel comprising:

a switching element;

a nonvolatile memory element; and

a pixel electrode,

wherein:

an input terminal of the switching element is electrically connected to the source signal line;

an output terminal of the switching element is electrically connected to the ~~nonvolatile memory element and the pixel electrode~~;the nonvolatile memory element is electrically connected between the pixel electrode and the common electrode; and

a control terminal of the switching element is electrically connected to the gate signal line.

2. (Currently Amended) A display device comprising:

a ~~plurality of source signal lines~~ line;a ~~plurality of gate signal lines~~ line;a common electrode; anda ~~plurality of pixels arranged in matrix~~ pixel comprising a plurality of sub-pixels, each of the sub-pixels comprising:

a switching element;

a nonvolatile memory element; and

a pixel electrode,

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wherein:

an input terminal of the switching element is electrically connected to the source signal line;

an output terminal of the switching element is electrically connected to ~~the nonvolatile memory element and~~ the pixel electrode;

the nonvolatile memory element is electrically connected between the pixel electrode and the common electrode; and

a control terminal of the switching element is electrically connected to the gate signal line.

3. (Currently Amended) A display device comprising:

a plurality of source signal lines;

a plurality of gate signal lines;

a common electrode; and

a plurality of pixels arranged in matrix pixel comprising a plurality of sub-pixels, each of the

sub-pixels comprising:

a switching element;

a nonvolatile memory element; and

a pixel electrode,

wherein:

an input terminal of the switching element is electrically connected to corresponding one of the plurality of source signal line lines;

an output terminal of the switching element is electrically connected to ~~the nonvolatile memory element and~~ the pixel electrode;

the nonvolatile memory element is electrically connected between the pixel electrode and the common electrode;

a control terminal of the switching element is electrically connected to the gate signal line; and

each switching element in ~~one of the pixels~~ the pixel is electrically connected to different one of the plurality of source signal lines.

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4. (Currently Amended) A display device comprising:
a ~~plurality of n lines of~~ source signal lines;
a ~~plurality of gate signal lines~~ line;
a common electrode; and
a ~~plurality of pixels arranged in matrix pixel~~ comprising n sub-pixels, each of the sub-pixels comprising:
a switching element;
a nonvolatile memory element; and
a pixel electrode,
wherein:
n lines of the source signal lines are corresponding to one pixel column;
an input terminal of the switching element is electrically connected to corresponding one of the n lines of source signal line lines;
an output terminal of the switching element is electrically connected to ~~the nonvolatile memory element and the pixel electrode~~;
the nonvolatile memory element is electrically connected between the pixel electrode and the common electrode;
a control terminal of the switching element is electrically connected to the gate signal line; and
each switching element in ~~one of the pixels~~ the pixel is electrically connected to corresponding one of the n lines of the source signal lines.

5. (Currently Amended) A display device comprising:
a ~~plurality of~~ source signal lines ~~line~~;
a plurality of gate signal lines;
a common electrode; and
a ~~plurality of pixels arranged in matrix pixel~~ comprising a plurality of sub-pixels, each of the sub-pixel comprising:
a switching element;
a nonvolatile memory element; and

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a pixel electrode,

wherein:

an input terminal of the switching element is electrically connected to the source signal line;

an output terminal of the switching element is electrically connected to ~~the nonvolatile memory element and the pixel electrode;~~

the nonvolatile memory element is electrically connected between the pixel electrode and the common electrode;

a control terminal of the switching element is electrically connected to corresponding one of the plurality of gate signal lines; and

each switching element in ~~one of the pixels~~ the pixel is electrically connected to different one of the plurality of gate signal lines.

6. (Currently Amended) A display device comprising:

~~a plurality of source signal lines;~~

~~a plurality of n lines of gate signal lines;~~

a common electrode; and

~~a plurality of pixels arranged in matrix~~ pixel comprising n sub-pixels, each sub-pixels comprising:

a switching element;

a nonvolatile memory element; and

a pixel electrode,

wherein:

n lines of the gate signal lines are corresponding to one pixel row;

an input terminal of the switching element is electrically connected to the source signal line;

an output terminal of the switching element is electrically connected to ~~the nonvolatile memory element and the pixel electrode;~~

the nonvolatile memory element is electrically connected between the pixel electrode and the common electrode;

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a control terminal of the switching element is electrically connected to corresponding one of the n lines of gate signal lines; and

each switching element in ~~one of the pixels~~ the pixel is electrically connected to corresponding one of the n lines of the gate signal lines.

7. (Currently Amended) A display device comprising:

~~a plurality of source signal lines~~ line;

~~a plurality of gate signal lines~~ line;

~~a common electrode~~; and

~~a plurality of pixel arranged in matrix, each of the pixels~~ pixel comprising:

a switching element;

a nonvolatile memory element;

a driver element; and

a pixel electrode,

wherein:

an input terminal of the switching element is electrically connected to the source signal line;

an output terminal of the switching element is electrically connected to ~~the nonvolatile memory element and the driver element~~;

the nonvolatile memory element is electrically connected between the pixel electrode and the common electrode;

a control terminal of the switching element is electrically connected to the gate signal line; and

the driver element is electrically connected to the pixel electrode.

8. (Currently Amended) A display device comprising:

~~a plurality of source signal lines~~ line;

~~a plurality of gate signal lines~~ line;

~~a common electrode~~; and

~~a plurality of pixels arranged in matrix~~ pixel comprising a plurality of sub-pixels, each of the sub-pixels comprising:

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a switching element;
a nonvolatile memory element; and
a driver element; and a pixel electrode,

wherein:

an input terminal of the switching element is electrically connected to the source signal line,

an output terminal of the switching element is electrically connected to ~~the nonvolatile memory element and the driver element;~~

the nonvolatile memory element is electrically connected between the pixel electrode and the common electrode;

a control terminal of the switching element is electrically connected to the gate signal line; and

the driver element is electrically connected to the pixel electrode.

9. (Currently Amended) A display device comprising:

a plurality of source signal lines;

~~a plurality of gate signal lines~~ line;

a common electrode; and

~~a plurality of pixels arranged in matrix~~ pixel comprising a plurality of sub-pixels, each of the sub-pixels comprising:

a switching element;

a nonvolatile memory element;

a driver element; and

a pixel electrode,

wherein:

an input terminal of the switching element is electrically connected to corresponding one of the plurality of source signal line lines;

an output terminal of the switching element is electrically connected to ~~the nonvolatile memory element and the driver element;~~

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the nonvolatile memory element is electrically connected between the pixel electrode and the common electrode;

a control terminal of the switching element is electrically connected to the gate signal line;

the driver element is electrically connected to the pixel electrode; and

each switching element in ~~one of the pixels~~ the pixel is electrically connected to different one of the plurality of source signal lines.

10. (Currently Amended) A display device comprising:

~~a plurality of n lines of~~ source signal lines;

~~a plurality of gate signal lines~~ line;

a common electrode; and

~~a plurality of pixels arranged in matrix~~ pixel comprising n sub-pixels, each of the sub-pixels

comprising:

a switching element;

a nonvolatile memory element;

a driver element; and

a pixel electrode;

wherein:

n lines of the source signal lines are corresponding to one pixel column

an input terminal of the switching element is electrically connected to corresponding one of the n lines of source signal ~~line~~ lines;

an output terminal of the switching element is electrically connected to ~~the nonvolatile memory element and the driver element;~~

the nonvolatile memory element is electrically connected between the pixel electrode and the common electrode;

a control terminal of the switching element is electrically connected to the gate signal line;

the driver element is electrically connected to the pixel electrode; and

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each switching element in ~~one of the pixels~~ the pixel is electrically connected to corresponding one of the n lines of the gate signal lines.

11. (Currently Amended) A display device comprising:

~~a plurality of source signal lines~~ line;

a plurality of gate signal lines;

a common electrode; and

~~a plurality of pixels arranged in matrix~~ pixel comprising a plurality of sub-pixels, each of the sub-pixels comprising:

a switching element;

a nonvolatile memory element;

a driver element; and

a pixel electrode,

wherein:

an input terminal of the switching element is electrically connected to the source signal line;

an output terminal of the switching element is electrically connected to ~~the nonvolatile memory element and the driver element~~;

the nonvolatile memory element is electrically connected between the pixel electrode and the common electrode;

a control terminal of the switching element is electrically connected to corresponding one of the plurality of gate signal line lines;

the driver element is electrically connected to the pixel electrode; and

each switching element in ~~one of the pixels~~ the pixel is electrically connected to different one of the plurality of gate signal lines.

12. (Currently Amended) A display device comprising:

~~a plurality of source signal lines~~ line;

~~a plurality of n lines of gate signal lines~~;

a common electrode; and

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~~a plurality of pixels arranged in matrix~~ pixel comprising n sub-pixels, each of the sub-pixels comprising:

- a switching element;
- a nonvolatile memory element; a driver element; and
- a pixel electrode,

wherein:

n lines of the gate signal lines are corresponding to one pixel row;
an input terminal of the switching element is electrically connected to the source signal line;

an output terminal of the switching element is electrically connected to ~~the nonvolatile memory element and driver element~~;

the nonvolatile memory element is electrically connected between the pixel electrode and the common electrode;

a control terminal of the switching element is electrically connected to corresponding one of the n lines of gate signal line lines;

the driver element is electrically connected to the pixel electrode; and

each switching element in ~~one of the pixels~~ the pixel is electrically connected to any one of the n lines of the gate signal lines.

13. (Original) A display device according to any one of claims 1 to 12, wherein a ferroelectric memory is utilized as the nonvolatile memory element.

14. (Original) A display device according to any one of claims 1 to 12, wherein a thin film transistor is utilized as the switching element.

15. (Original) A display device according to any one of claims 1 to 12, wherein the source signal line driver circuit is formed on the same substrate as the pixel.

16. (Original) A display device according to any one of claims 1 to 12, wherein the gate signal line driver circuit is formed on the same substrate as the pixel.

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17. (Original) A display device according to claim 15, wherein the source signal line driver circuit or the gate signal line driver circuit is configured with unipolar transistors.

18. (Original) A display device according to claim 16, wherein the source signal line driver circuit or the gate signal line driver circuit is configured with unipolar transistors.

19. (Original) Electric apparatuses, wherein the display device as set forth in any one of claims 1 to 12 is applied.

20. (Currently Amended) A display device comprising:

a ~~plurality of source signal lines line~~;

a ~~plurality of gate signal lines line~~; and

a ~~plurality of pixels arranged in matrix, each of the pixels pixel~~ comprising:

a first switching element;

a second switching element;

a capacitor element;

a nonvolatile memory element; and

a pixel electrode,

wherein:

an input terminal of the first switching element is electrically connected to the source signal line;

an output terminal of the ~~second~~ first switching element is electrically connected to an input terminal of the second switching element and the pixel electrode;

a control terminal of the first switching element is electrically connected to the gate signal line; and

the second switching element is selectively connected to one of the capacitor element and the nonvolatile memory element.

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